







- IMX542 CMOS sensor
- 16.2 MP resolution
- ALVIUM image processing
- GMSL2 interface
- Various hardware options

Model without hardware options

### Robust CSI-2 based Alvium cameras with GMSL2 interface

Benefit from greater flexibility in cable lengths

Alvium GM2 STP cameras with GMSL2 (Gigabit Multimedia Serial Link) interface have been designed to overcome the limitations of standard CSI-2 cameras. The closed housing CSI-2 based cameras come with integrated serializer and a rugged HSD STP connector for cable lengths up to 8 meters. This connection can also be used to power cameras (Power over STP), enabling single cable solutions.

To operate Alvium GM2 cameras on your vision system, Allied Vision provides different access modes: - GenlCam for CSI-2 Access controls the camera by GenlCam features, using the Alvium CSI-2 driver and CSI-2 transport layer (TL) directly. All Alvium GM2 STP models with equivalent 1800 C models are supported. Please find FAQs and installation instructions in the Getting Started with GenlCam for CSI-2 application note. - Direct Register Access (DRA) to control the cameras via registers for advanced users. - Video4Linux2 Access allows to control the cameras via established V4L2 API and applications like GStreamer and OpenCV. Open-source CSI-2 drivers are available on GitHub for different boards and systems on chip (SoCs).

In addition to lens mount and housing options, see Customization and OEM Solutions webpage for additional options.



# Specifications

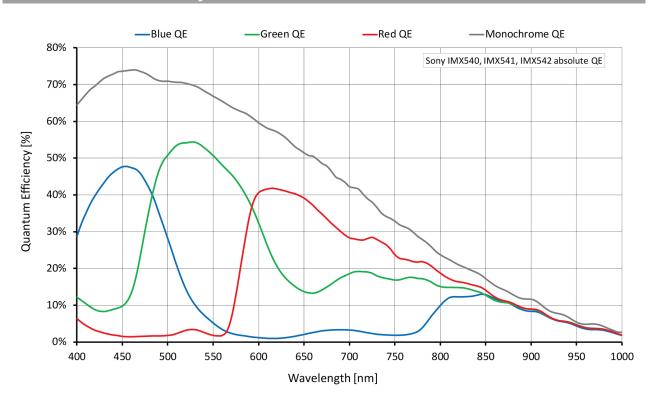
InterfaceGMSL2, based on MIPI CSI-2, up to 4 lanesResolution512 (H) × 3040 (V)Spectral range300 to 1100 nmSensorSony IMX542Sensor typeCMOSShutter modeGS (Global shutter)Sensor sizeType 1.1Pixel size2.74 µm × 2.74 µmLens mounts (available)Mainly depends on hardware and register settings.ADC12 Bit		
Spectral range300 to 1100 nmSensorSony IMX542Sensor typeCMOSShutter modeGS (Global shutter)Sensor sizeType 1.1Pixel size2.74 µm × 2.74 µmLens mounts (available)C-Mount, CS-MountMax. frame rate at full resolutionJaing depends on hardware and register settings.ADC12 Bit	Interface	GMSL2, based on MIPI CSI-2, up to 4 lanes
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ADC 12 Bit	Lens mounts (available)	C-Mount, CS-Mount
	Max. frame rate at full resolution	Mainly depends on hardware and register settings.
Image buffer (RAM) 256 KByte	ADC	12 Bit
	Image buffer (RAM)	256 KByte
Non-volatile memory (Flash) 1024 KByte	Non-volatile memory (Flash)	1024 KByte

Output	
Bit depth	12-bit
YUV color pixel formats	YUV422 8-bit (UYVY) [MIPI CSI-2 (FOURCC)]
RGB color pixel formats	RBG888 (RGB3) [MIPI CSI-2 (FOURCC)]
Raw pixel formats	RAW8 (GREY), RAW10 (Y10), RAW12 (Y12) [MIPI CSI-2 (FOURCC)]

General purpose inputs/outputs (GPIOs)		
TTL I/Os	2 programmable GPIOs	
Operating conditions/dimensions		
Operating temperature	-20 °C to +65 °C (housing)	
Power requirements (DC)	5 VDC over MIPI CSI-2	
Power consumption	Value for the integrated serializer adds to CSI-2 model value.	
Mass	70 g	
Body dimensions (L $\times$ W $\times$ H in mm)	41 × 29 × 29	



Quantum efficiency





### Features

#### Image control: Auto

- Auto exposure
- Auto gain
- Auto white balance (color models)

#### Image control: Other

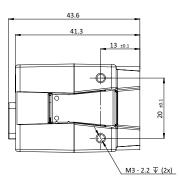
- Black level
- Color transformation (incl. hue, saturation; color models)
- De-Bayering up to 5×5 (color models)
- DPC (defect pixel correction)
- FPNC (fixed pattern noise correction)
- Gamma
- Reverse X/Y
- ROI (region of interest)

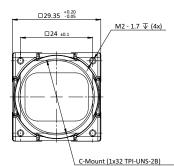
#### Camera control

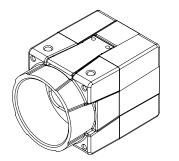
- Acquisition frame rate
- Firmware update in the field
- I/O and trigger control
- Temperature monitoring

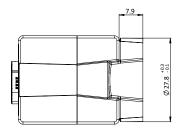


## Technical drawing









13 ±0.

20 ±0.1

M3 - 2.2 ∓ (2x)

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